

11/20/01

Class	Subclass		
ISSUE CLASSIFICATION			

PATENT NUMBER

U.S. **UTILITY** Patent Application

<div style="display: flex; justify-content: space-between;"> <div>  SCANNED </div> <div> O.I.P.E.  </div> <div> Q.A. <u>Am</u> </div> </div>	<div style="display: flex; justify-content: space-between;"> <div> PATENT DATE </div> <div>  </div> </div>
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APPLICATION NO. 09/989666	CONT/PRIOR	CLASS 257	SUBCLASS	ART UNIT 2811	EXAMINER H. Van <i>[Signature]</i>
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APPLICANTS

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3.1.11

Structure and method for wiring translation between grids with non-integral pitch ratios in chip carrier modules

PTO-2040
12/99

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ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg	Figs. Drwg	Print Fig	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____					
				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
				ISSUE BATCH NUMBER	

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